

## InnoLogic BE5001

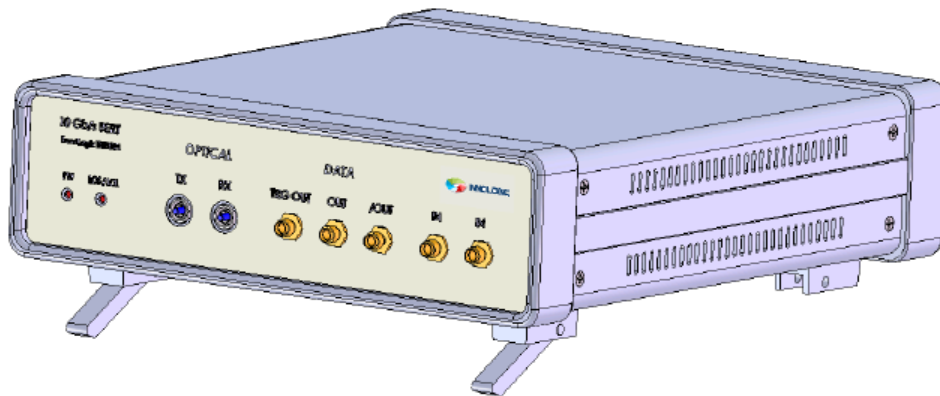
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# 10 Gb/s BERT

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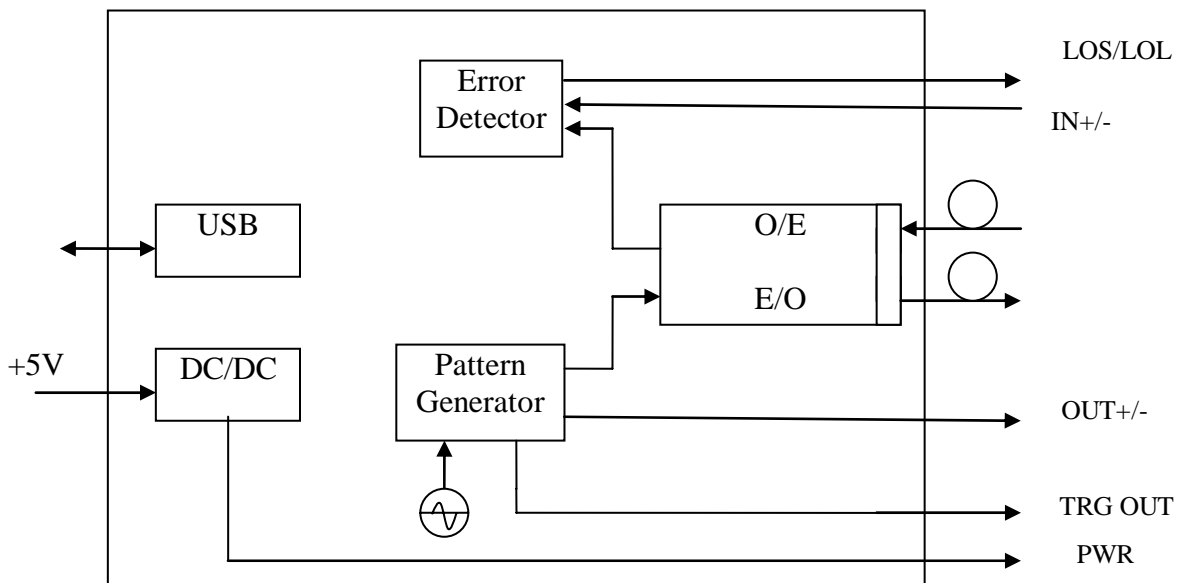
### Features

- **Multiple Data Rates at 10G: OC192/10GbE/FEC/WAN**
- **Electrical and Optical Interfaces**
- **PRBS and User Pattern**
- **Small Package**
- **Ultra Low Power Consumption**
- **Low Cost Solution for Production Testing**
- **Controlled by User Software on External PC Connected by USB Interface**
- **Free Labview Driver**



InnoLogic Technology is an established global technology company that provides test solutions and software services for communications industry. With our unique insight and background within telecom and datacom industry, we delivers low cost, high performance test instruments and automatic test sets for high-speed communications components design and manufacturing. InnoLogic BE5001 10G BERT is a high performance, low cost equipment suitable for transceiver and TOSA/ROSA production testing.

### Functional Block Diagram



### System Specification

#### Data rate

The same data rate applies for pattern generator electrical out and optical out, and for error detector electrical in and optical in.

OC192	9.95328 Gb/s
10GBE LAN	10.3125 Gb/s
OC192 with FEC	10.709225 Gb/s
10GBE LAN with FEC	11.095727 Gb/s



Accuracy	$\pm 50$ ppm
Any-rate	9.9Gb/s~11Gb/s (with 1/64 external reference clock)

### **Operating system**

The GUI software supplied runs on Windows 2000 or XP by a USB interface.

### **Pattern Generator**

Pattern	PRBS7, PRBS31, 64bit user pattern
Polarity	Inverted or non-inverted
Electrical interface	SMA, differential, 100ohm, AC coupled
Output swing:	380mV ~ 770mVpp Typical
Optical interface	Support 850nm, 1310nm, 1550nm
External reference clock Input	1/64 clock (optional to support any-rate from 9.9Gb/s-11Gb/s)
Trigger Output	1/64 clock

### **Error Detector**

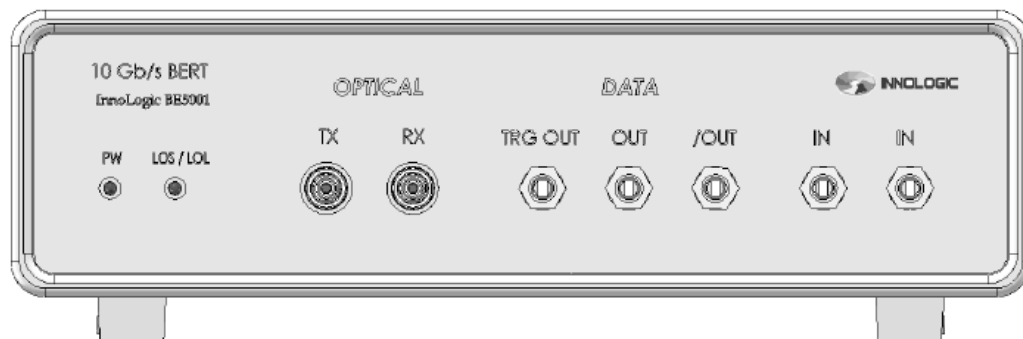
Pattern	PRBS7, PRBS31, 64 bit user pattern
Polarity	Inverted or non-inverted
Electrical interface	SMA, differential, 100ohm, AC coupled
Input sensitivity	8mV~1000mVpp
Optical interface	Support 850n, 1310nm, 1550nm
Clocking mode	Integrated CDR

### **General**

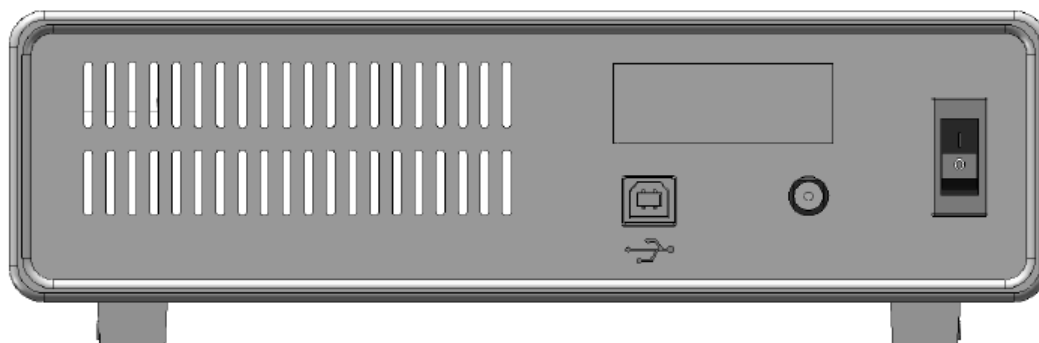
Power Supply	DC +5V jacket with AC/DC adaptor (Ultra low power consumption)
Power consumption	3W Max
Operating temperature	0°C to 55°C

## Outline

### Front Panel

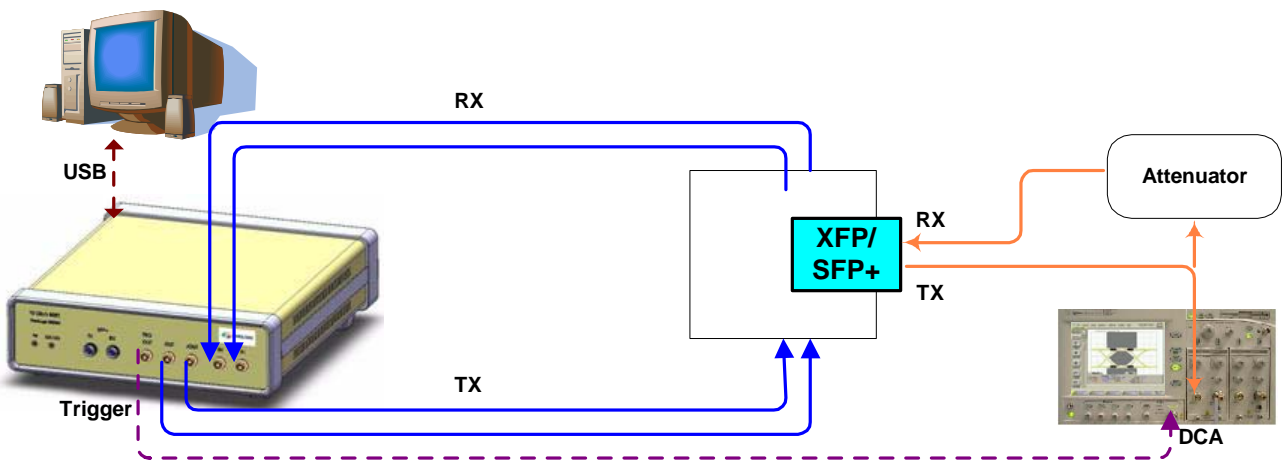


### Rear Panel

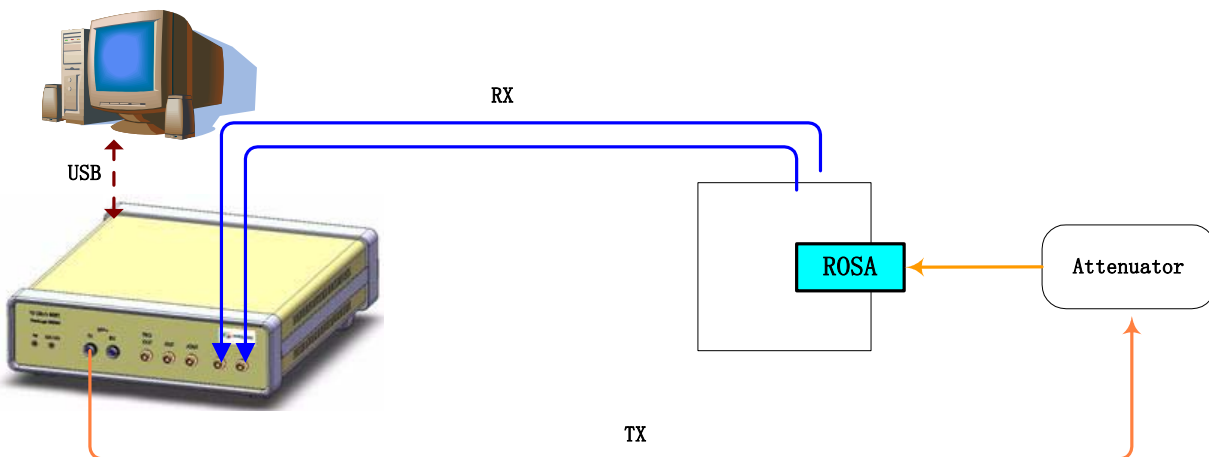


## Application

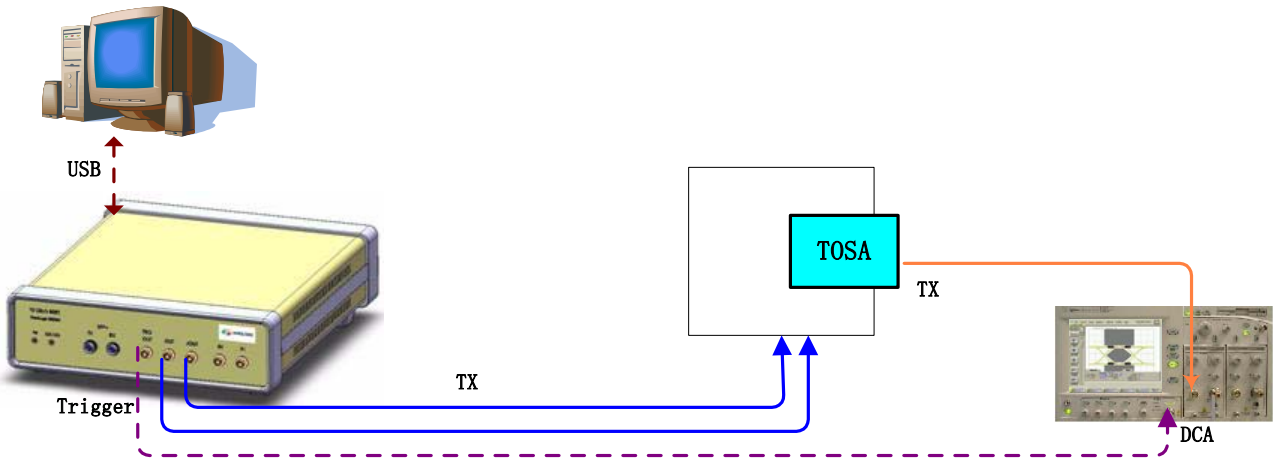
- **10G XFP/SFP+ Test Diagram**



- **10G ROSA Sensitivity Test Diagram**



- **10G TOSA High Speed Performance Test Diagram**



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